



10-09-02

Receipt
2800
#10PATENT
Docket No. 81754.0048

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Toshihiko HIGUCHI

Serial No.: 09/754,818

Filed: January 4, 2001

For: SEMICONDUCTOR DEVICE HAVING A
WIRING PATTERN AND METHOD FOR
MANUFACTURING THE SAME (As Amended)

Art Unit: 2814

Examiner: Cao, P.

I hereby certify that this correspondence
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Commissioner for Patents
Washington D.C. 20231, on

September 23, 2002

Date of Deposit

Kimberly Yee

Name

Kimberly Yee 09/23/02

Signature

Date

REQUEST FOR CORRECTED FILING RECEIPTCommissioner for Patents
Washington, D.C. 20231

Dear Sir:

Applicant respectfully acknowledges receipt of the Updated Filing Receipt dated October 9, 2001. However, corrections need to be made to the October 9, 2001 Updated Filing Receipt in order to correct the Group Art Unit, and the Title of the Invention. On September 16, 2002, Attorneys for Applicant filed an Amendment in which request was made for a replacement of the title of the invention. Applicant respectfully requests that a Corrected Filing Receipt be issued reflecting these changes as follows:

GROUP ART UNIT: 2814

TITLE OF THE
INVENTION:Semiconductor Device Having a Wiring Pattern
and Method for Manufacturing the Same

For convenience, a photocopy of the Filing Receipt with the corrections marked thereon is attached.

No fee is required for this request. However, if a fee should be required, please charge it to Deposit Account No. 50-1314 of the undersigned attorney's firm. A copy of this Request is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: September 23, 2002

By: 

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UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
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WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/754,818	01/04/2001	4766 2814	1370	81754.0048	8	20	8

CONFIRMATION NO. 3513

UPDATED FILING RECEIPT



OC000000006865293

26021
HOGAN & HARTSON L.L.P.
500 S. GRAND AVENUE
SUITE 1900
LOS ANGELES, CA 90071-2611

Date Mailed: 10/09/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Toshihiko Higuchi, Yamanashi-ken, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2000-000935 01/06/2000
JAPAN 2000-394959 12/26/2000

If Required, Foreign Filing License Granted 06/08/2001

Projected Publication Date: 01/17/2002

Non-Publication Request: No

Early Publication Request: No

Title

having a wiring pattern
Semiconductor device and method for manufacturing the same

Preliminary Class

438

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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